

Registration No:

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Total Number of Pages: 02

Course: IDD (B.Tech and M.Tech)  
Sub\_Code: 23ES1002

2<sup>nd</sup> Semester Regular/Back Examination: 2024-25

SUBJECT: BASIC ELECTRONICS

BRANCH(S): AE, AEIE, AERO, AUTO, BIOMED, BIOTECH, CE, CHEM, CIVIL, CSE, CSEAI, CSEAIML, CSEDS, CSIT, CST, ECE, EEE, ELECTRICAL, ELECTRICAL & C.E, ELECTRONICS & C.E, ETC, MANUTECH, MECH, METTA, MME, PLASTIC

Time: 3 Hours

Max Marks: 100

Q.Code: S613

Answer Question No.1 (Part-I) which is compulsory, any eight from Part-II and any two from Part-III.

The figures in the right hand margin indicate marks.

**Part-I**

**Q1 Answer the following questions: (2 x 10)**

- What is forbidden energy gap? What is its magnitude for Ge and Si Semiconductor?
- Determine the forward resistance of a silicon diode when the forward current is 4 mA at  $T = 300\text{K}$
- The reverse gate voltage of JFET when changes from 4.4 V to 4.2 V, the drain current changes from 2.2 mA to 2.4 mA. Find out the value of Transconductance of the JFET.
- What is the relation between  $I_{CO}$  and  $I_{CEO}$  in a transistor?
- Why the input impedance of a FET is more than that of a BJT?
- What is virtual ground concept in OPAMP?
- Calculate CMRR in decibel for the OPAMP circuit measurement of  $V_d = 1\text{ mV}$ ,  $V_0 = 120\text{ mV}$  and  $V_C = 1\text{ mV}$ ,  $V_0 = 20\text{ }\mu\text{V}$
- Convert  $(35.45)_{10} = (\text{---})_2$  and  $(AE.2B)_{16} = (\text{---})_8$
- How does negative feedback influence input and output impedances in a voltage-series feedback amplifier?
- If an AM signal has a carrier amplitude of 100 V and a modulating signal amplitude of 20 V, what is the modulation index?

**Part-II**

**Q2 Only Focused-Short Answer Type Questions- (Answer Any Eight out of Twelve) (6 x 8)**

- A Silicon diode has reverse saturation current of  $2.5\text{ }\mu\text{A}$  at 300 K. Find forward voltage for a forward current of 10 mA.
- Simplify the Boolean expression  $F = ABC + A\bar{B}(\bar{A}\bar{C})$  using Boolean laws and draw the logic diagram of the output expression using logic gates?
- With neat sketch, explain the operation of a center tapped full wave rectifier.