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Total Number of Pages : 02

B.Tech/  
Integrated Dual Degree (B.Tech and M.Tech)  
RBL2B002

2<sup>nd</sup> Semester Reg. / Back Examination: 2022-2023

Basic Electronics Engineering

AERO, AE, AUTO, BIOTECH, CHEM, CIVIL, CST, CSEAI, CSEDS, CSE, CSIT, CSEAIME, ELECTRICAL & C.E, EEE, ELECTRICAL, ECE, ETC, EIE, MANUTECH, MECH, MME, METTA, MINERAL, MINING, PLASTIC, IT

Time : 3 Hour

Max Marks : 100

Q.Code : M383

Answer Question No.1 (Part-1) which is compulsory, any eight from Part-II and any two from Part-III.

The figures in the right hand margin indicate marks.

Part-I

- Q1 Answer the following questions: (2 x 10)
- Draw the equivalent circuit diagram of a diode.
  - Draw V-I characteristics of the ideal zener diode.
  - Write the applications of CE, CB, CC configuration of transistors.
  - Define slew rate.
  - Distinguish between BJT and FET.
  - Draw the circuit diagram of an Op Amp differentiator.
  - Write 4 applications of closed loop Op amp circuits.
  - Draw logic gate symbols for NOR & X-OR gates.
  - Draw the OR gate using NAND gates.
  - $(127)_{10} = ( ? )_8$  and  $(110110)_2 = ( ? )_{16}$

Part-II

- Q2 Only Focused-Short Answer Type Questions- (Answer Any Eight out of Twelve) (6 x 8)
- Draw the VI characteristics of a diode and explain about its current equation.
  - Explain the principle of operation of a pnp transistor.
  - What is a zener diode? Explain about its constructional details with applications.
  - Explain the operation of a digital inverter.
  - Explain about MOSFET and its characteristics.
  - Explain the principle of operation of a JFET.
  - Design a circuit which produces the output voltage  $V_0 = 2V_1 - 6V_2 + 9V_3$  using Op-amp with minimum resistance value  $50k\Omega$ .
  - Write the ideal characteristics of Op-amp, with its physical interpretation.
  - Derive the output voltage of a differentiator circuit using Op-amp.

- j) Design a full adder using NOR gates only.
- k) Design a X-OR gate using minimum number of NOR gates.
- l) Explain about number systems and its conversion details.

**Part-III**

**Only Long Answer Type Questions (Answer Any Two out of Four)**

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|-----------|--|-------------|
| <b>Q3</b> | Explain various types of transistors, its constructional details and input output characteristics. | <b>(16)</b> |
| <b>Q4</b> | Explain in detail about CMOS, its constructional details, merits and applications.                 | <b>(16)</b> |
| <b>Q5</b> | What is an op-amp, its equivalent circuit, applications with neat circuits.                        | <b>(16)</b> |
| <b>Q6</b> | Design a half adder, full adder, full subtractor with NAND gates only.                             | <b>(16)</b> |